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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Attn: OIPE

Eiichi TAKAHASHI et al.

Group Art Unit: 2816

Application No.: 10/520,429

Docket No.: 122358

Filed: July 13, 2005

For: DIGITAL CIRCUIT HAVING DELAY CIRCUIT FOR ADJUSTMENT OF CLOCK
SIGNAL TIMING

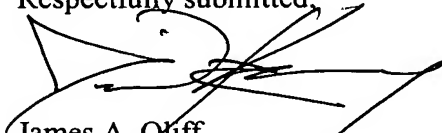
REQUEST FOR CORRECTION OF PALM RECORDS

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Sir:

Attached is a photocopy of the original filing receipt on which errors have been corrected in red. These errors are being brought to the attention of the Patent and Trademark Office so that it may correct its records.

Respectfully submitted,


James A. Oliff
Registration No. 27,075

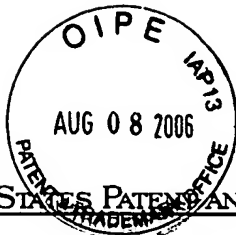
David R. Kemeny
Registration No. 57,241

JAO:DRK/mrs

Date: August 8, 2006

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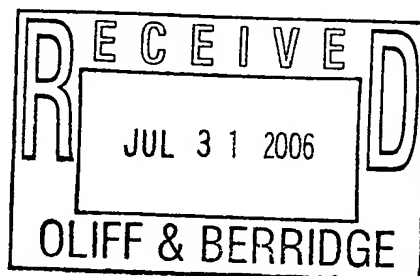


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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/520,429	07/13/2005	2816	515	122358	19	9	1

25944
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CONFIRMATION NO. 5260
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OC000000019733534

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Applicant(s)

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→ Assignment for Published Patent Application: National Institute of Advanced Industrial Science and Technology, Tokyo, JAPAN

Power of Attorney: The patent practitioners associated with Customer Number 25944.

Domestic Priority data as claimed by applicant

This application is a 371 of PCT/JP03/08648 07/08/2003

Foreign Applications

JAPAN 2002-200467 07/09/2002

If Required, Foreign Filing License Granted: 07/24/2006

The country code and number of your priority application, to be used for filing abroad under the Paris Convention, is **US10/520,429**

Projected Publication Date: Not Applicable

Non-Publication Request: No

Early Publication Request: No

** SMALL ENTITY **

Title

Digital circuit having a delay circuit for adjustment of clock signal timing

Preliminary Class

327

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